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Procedure Configure the oscilloscope instrument to capture several periods of the input phase and the output phase. Modify the single mirror in a Wilson current mirror, as shown in Figure 10. Widlar current mirror waveforms, W2 at a frequency of 10 kHz, Graph Scopy. Graph 5. Now change the frequency from W1 to 200 Hz and trace the two waveforms. Another feature of the current mirror is a relatively low input resistance that helps to keep the input current constant regardless of the drive conditions. The simple cable that connects the Q1 manifold to its base is replaced by a relay tracking buffer. Materials: ADALM2000 [3] active learning module Weld-free jumper wires Two resistors of 1 k Â© (values equated as much as possible, or measured to three years or better) Two small-scale NPN transistors (2N3904 or SSM2212) One double-op amplifier (such as ADTL082) Two 4.7–¼-F decoupling capacitors Instructions A good way to measure the characters The current mirror uses the same basic configuration [3] used in BJT curve plotter experiments of emitter comNo. Graph 9. Graph 3. The current mirror is often used to provide bias currents and active loads in amplifier stages. Collector 2. Base 3. The Widlar circuit can be used with bipolar transistors or MOS transistors. Manufacture [3] Motorola Datasheet Download 2N3904 Recommended Third Party Data Sheet 2N3904 Data sheet Part 2N3904 Description FunciÂ n [3] of transistor NPN 2N3904 General [1] Transistors NPN T O-92 High Speed Switching Features[3]: 12 Ã¯1â 2Ã¯Ã122000 NPN planar silicon switching the thrusting torses. Important attributes for current sources include high output resistance with a wide range of voltage compliance and voltage rejection external such as the source of feeding or temperature. Motorola manufacture Data sheet Downloads 2N3904 Data sheet Part 2n3904 Description Function of the transistor transistor NPN Motorola Semiconductor Technical data G Enerral Gnee BASE 1 EMISOR MIX. MINIMUM CLASSIFICATION Symbol Value Co llector Â Emitter Voltage Collector Â 1⁄Z Â Base Voltage Emitter Â Base Voltage e Collector Current Â Continuous Total l Disipacity Device @ TA = 25°C Derat e above 25°C VCBO VEBO IC PD 40 60 6.0 200 625 5.0 Total Device Layout @ TC = 25°C Derat above 25°C to°C PD 1.5 12 Operation and storage Junction Temperature range TJ, Tstg Ã 5 5 to +150 TEMIC FEATURES (1) Characteristic symbol Max. Current mirror waveforms, W1 at 200 Hz, W2 at 40 Hz, as shown on an LTspice chart. Figure 6 shows an example of an LTspice simulation of the same circuit. The collector voltage is swept using a triangular waveform of AWG1 (output W1) with a frequency of 40 Hz. You can find the answer on the StudentZone blog. Can you name an advantage and a disadvantage of the Wilson current mirror? Figure 1. Figure 14. The simple implementation of two transistors of the current mirror is based on the fundamental relationship that two transistors of the same size at the same temperature with the same VBE have the same drainage current or collector. Checking circuit of the rear-view mirror of simple AC current. Â¢ Fast switching devices exhibi features of short off and low voltage saturation. The output current of the mirror is measured by the range inputs 2+ and 2 Â through the resistance 1 kÂ©, R2. Trace the two waveforms using the oscilloscope provided by the telescope tool or through the LTspice simulation. Current mirror waveforms, W2 at 10 kHz frequency, as shown on a Scopy chart. Issuer Page 1 12.05.08 V1.1 2N3904 General Purposes and Transistors NPN TO-92 Absolute Maxim um Ratings Description . 2N3904 is an NPN transistor for general applications. Figure 15. Figure 7. Part number: 2N3904 Functions: Pack NPN NPN rotsisnart ed n³ÄicnuF n³ÄicpircseD 4093N2 etraP 6093N2 noc oiratnemelpmoc raP ¢ÄÂ ¢Â rotceloc led adilas ed dadicapaC ¢ÄÂ ¢Â rotceloc led n³Äicarutas ed n³Äisnet ajaB ¢ÄÂ ¢Â serutaeF egaml mhoR ,PXN ,rotcudnocimeS NO :etnacirbaF opiT 29 A :NPN lareneg osu ed oicilis ed DATA TECHNICAL OF SEMICONDUCTORS G General purpose [3] transistors NPN Silicon Emitter 3 2N3903 2N3904* *Motorola P device referred to 2 BASE 1 EMITTER MAX IMUM RATINGS Rating Symbol Value Co reader Â¢ Emitter Voltage Collector Ã´Â Â Â½ Base Voltage Emitter Â¢ Base Voltage e Collector Current Â¢ Continuous Tota l Device Dissipation @ TA = 25Â Â°C Derat above 25Â Â°C VCEO VCBO VEBO IC PD 40 60 6.0 200 625 5.0 Total device layout @ TC = 25Â Â°C Derate above 25Â Â°C PD 1.5 12 Operation and storage Junc tion Temperature range TJ, Tstg Â¢ Â Ã 5 a +150 CHARACTERÂ STICAS TÃo RMICAS(1) Symbolic symbol Max. Figure 8. ConexiÂ n the bread board of the power mirror test circuit (with op amplifier). The input and output resistors R1 and R2 are now 1 kÂ Â©. Figure 4. Figure 10. Figure 11. Wilson Power Mirror. Hardware Setup Load the stairstep.csv file for the W2 channel of the selectable generator and adjust the amplitude to 3 V p-p with the offset set to 1.5 V. TO-92 Plastic Package Dimensions A B C D E F G H K Minimum 4.32 4.45 3.18 0.41 0.35 1.14 12.70 Maximum 5.33 5.20 4.19 0.55 0.50 5Â * 1.40 1.53 Dose : Set up [3] thousand-meter pin: 1. The current gain of the follow-emitting buffer stage (Q2) greatly reduces the gain error caused by the finite base currents of Q1 and Q2. An example of Scopy plotting is provided in Figure 12. An important feature of the current doubling[3] is a relatively high output resistance that helps to keep the output current constant regardless of load conditions. This enhancement of the single-current mirror is known as an augmented emitter mirror. The manifold voltage is swept using a triangular waveform of AWG 1 (W1 output) at a frequency of 40 Hz. frequency.

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